

TITLE OF THE INVENTION

PLASMA DISPLAY MODULE

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention relates to a plasma display module to be integrated into a plasma display and, more particularly, to a plasma display module wherein a power circuit can be easily designed.

10 Description of the Related Art

Fig. 1 is a block diagram showing a prior plasma display. A power circuit 8 is provided to the prior plasma display. The power circuit 8 includes a power circuit for driving a plasma display module and a power circuit for driving an interface board. The interface board is used to, for example, analog/digital convert an analog image input signal and output a digital image signal.

A source voltage necessary to drive the interface board is designed by a plasma display manufacturer and depends on an integrated circuit (IC) which has been customized for each manufacturer. Accordingly, the source voltage varies from one manufacturer to another and has not been standardized. For example, the source voltage can be 3.3V, 5V, 7V, or 12V.

25 Accordingly, in a case where the plasma display manufacturer develops a power source, it is necessary to newly develop an integrated power circuit to drive both the interface board and plasma display module.

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5 On the other hand, as a power source to drive the plasma display module, a high-voltage and high-capacity power source is necessary. Therefore, in order to develop such an integrated power circuit, there is a problem in that a great amount of costs and man-hours become necessary.

#### SUMMARY OF THE INVENTION

10 It is an object of the present invention to provide a plasma display module for which the development costs and man-hours for development of a power circuit by a plasma display manufacturer can be reduced.

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15 A plasma display module according to the present invention comprises: a plasma display panel; driving circuits which drives the plasma display panel; and a power circuit into which an external alternating current is inputted from outward. The power circuit supplies driving voltages to the driving circuits. The power circuit outputs an external source voltage to be used by an external power circuit and a control voltage for controlling operations of an interface board to which source voltages of the interface  
20 board are supplied from the external circuit. Operations of the power circuit are controlled with control signals output by the interface board.

25 In the present invention, the power circuit for supplying a source voltage to the driving circuit is provided in the plasma display module, accordingly the power circuit is supplied by a plasma display module manufacturer. Therefore, when a plasma display manufacturer develops a plasma display, this can be carried out by only designing an

interface board which A/D converts an analogue image input signal and outputs a digital image signal and an external power circuit for driving the the interface board.

Accordingly, the development costs and man-hours for development of the power circuit can be reduced.

In addition, since the power circuit is delivered after being integrated into a plasma display module, the plasma display manufacturer can omit adjustment of the driving voltages. Also therein, man-hours can be reduced.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram showing a prior plasma display panel.

Fig. 2 is a block diagram showing a construction of a plasma display module with a power source according to an embodiment of the present invention.

Fig. 3 is a block diagram showing a plasma display using the plasma display module with a power source shown in Fig. 2.

Fig. 4 is a timing chart showing operations of the plasma display shown in Fig. 3.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinafter, a preferred embodiment of the present invention will be described in detail with reference to the attached drawings. Fig. 2 is a block diagram showing a construction of a plasma display module with a power source according to an embodiment of the present invention.

A plasma display panel 2 and a driving circuit 3 for driving the plasma display panel 2 are provided to a plasma

*insert* display module 1.

A power circuit 4 which generates source voltages for driving the plasma display module 1 receives inputs of an AC power source, a control signal PSS, and a control signal PSM. The power circuit 4 outputs a control voltage Vstb, a power voltage Vaux, and a driving voltage group to be supplied to the driving circuit 3. The driving voltage group consists of a power voltage Vcc of, for example, 5V, a source voltage for data electrodes Vd of, for example, 60V, and a source voltage for sustaining electrodes of, for example, 160V, however, other voltages may be included in the driving voltage group.

Fig. 3 is a block diagram showing a plasma display using the plasma display module with a power source shown in Fig. 2. In a case where the plasma display module 5 with a power source shown in Fig. 2 is incorporated into a plasma display, for example, an interface board 7 which A/D converts an analogue image input signal and outputs a digital image signal, and a power circuit 6 which generates source voltages for driving the interface board 7 are provided. The source voltage Vaux is supplied to the power circuit 6 and the control voltage Vstb is supplied to the interface board 7. On the other hand, the control signals PSS and PSM are output from the interface board 7 to the power circuit 4. N types of source voltages Vx1 through Vxn are supplied from the power circuit 6 to the interface board 7. Voltages of, for example, 3.3V, 5V, and 7V are included in the source voltages Vx1 thorough Vxn. In addition, an

analogue image signal is inputted into the interface board 7. A digital image signal which is obtained through conversion by an A/D converter that is built in the interface board 7 is inputted into the driving circuit 3.

5           Thereafter, operations of the plasma display constructed as mentioned above will be described. Fig. 4 is a timing chart showing operations of the plasma display shown in Fig. 3.

10           First, a description will be given of operations when starting up the power source.

When the AC power source is inputted at time  $t_1$ , the control voltage  $V_{stb}$  becomes high level and the plasma display reaches a stand-by state.

15           When a power source of the plasma display is turned on at time  $t_2$ , a control portion of the interface board 7 starts to operate and the control signal PSS becomes high level.

20           The power circuit 4 makes, when the high-level control signal PSS is inputted thereto, the source voltage  $V_{aux}$  high level. In addition, the power circuit 6 makes, when the high-level source voltage  $V_{aux}$  is inputted thereto, the source voltages  $V_{x1}$  through  $V_{xn}$  high level. As a result, the interface board 7 starts A/D conversion.

25           The interface board 7 begins to supply the digital image signal to the driving circuit 3 and simultaneously makes the control signal PSM high level.

            The power circuit 4 makes, when the high-level control signal PSM is inputted thereto, the low source voltage  $V_{cc}$

high level and then makes the high power voltages  $V_d$  and  $V_s$  high level in turn. As a result, the plasma display panel 2 can display an image. If a high power voltage is started up earlier than a low power voltage, a high voltage circuit may have floating gate levels and a penetration current may flow, causing damage to the high voltage circuit. For the prevention of the damage, in the present embodiment, the high power voltages  $V_d$  and  $V_s$  are made high level after the low power voltage  $V_{cc}$  is made high level.

10        Thereafter, a description will be given of operations when shutting down the power source.

When a power source of the plasma display is turned off at time  $t_3$ , the interface board 7 stops outputting the digital image signal and simultaneously makes the control signal PSM low level.

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When the control signal PSM becomes low level, the power circuit 4 makes the source voltages  $V_d$  and  $V_s$  low level and then makes the source voltage  $V_{cc}$  low level.

Thereafter, after an elapse of a predetermined time of, for example, 200m seconds, the interface board 7 makes the control signal PSS low level.

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When the control signal PSS becomes low level, the power circuit 4 makes the source voltage  $V_{aux}$  low level. When the source voltage  $V_{aux}$  becomes low level, the power circuit 6 makes the source voltages  $V_{x1}$  through  $V_{xn}$  low level. As a result, the plasma display reaches a stand-by state.

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Thereafter, when the AC power source is disconnected

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